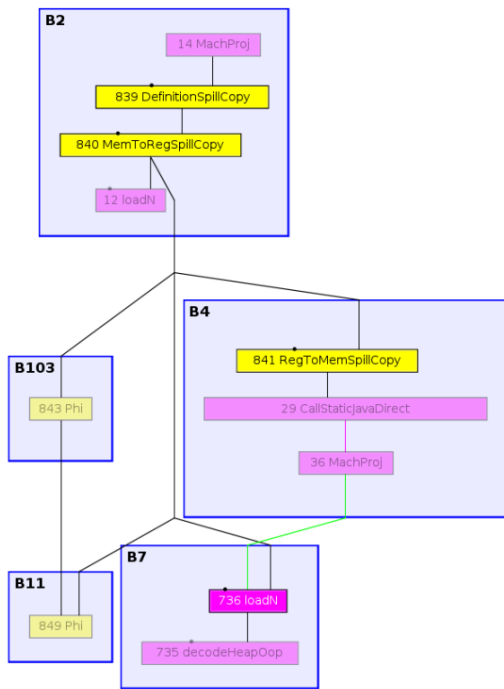
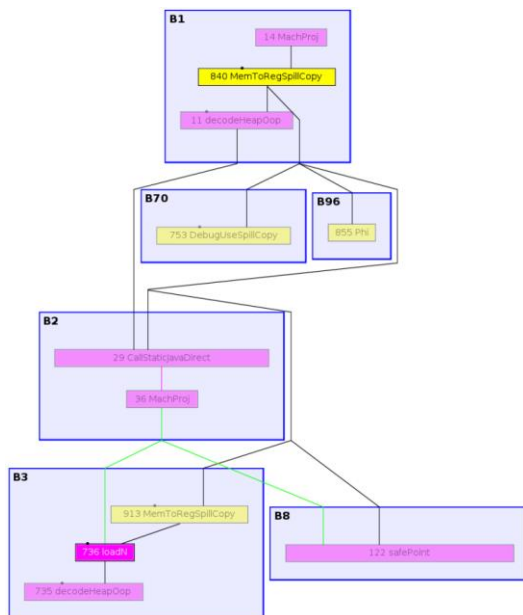


Before fix (after 1st Split)

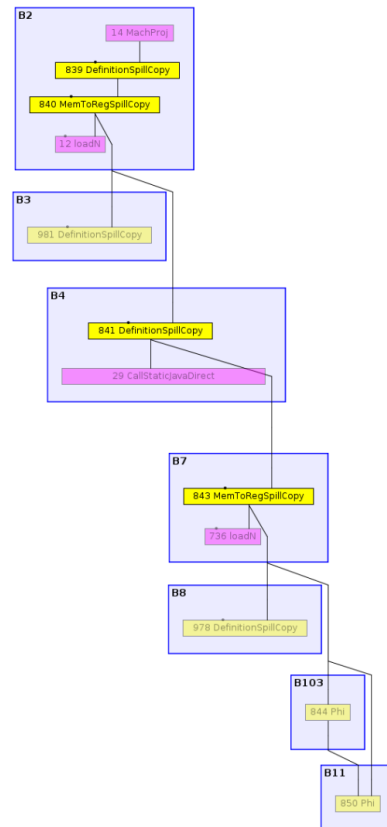


Before fix (Final code)

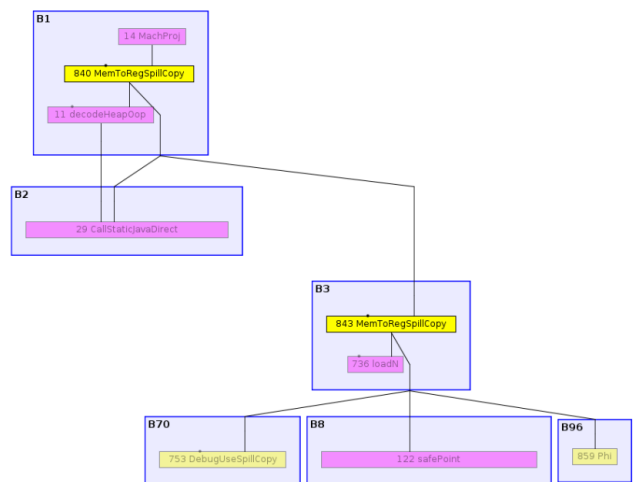


LRG of “840 MemToRegSpillCopy” covered almost the entire function, hence it interfered with more live ranges (e.g. “36 MachProj”). These interferences caused the LRG no freedom and lastly RA had to spill it to stack.

After fix (after 1st Split)



After fix (Final code)



LRG of “840 MemToRegSpillCopy” is split into two shorter live ranges by “841 DefinitionSpillCopy”. Both LRGs are assigned to registers due to fewer interferences.